

ABSTRACT OF THE DISCLOSURE

A method and apparatus for the transmission of data through multiple clock domains using synchronous and asynchronous FIFOs are described. In an embodiment, a method includes receiving data at a first data transfer rate. Additionally, the method includes storing the data at the first data transfer rate in a synchronous storage device having a first storage area. The data at the first data transfer rate stored in the synchronous storage device is processed. The processing includes removing the data from the synchronous storage device. The processing also includes storing the data at the first data transfer rate in an asynchronous storage device having a second storage area. Additionally, the processing includes transmitting the data out from the asynchronous storage device at a second data transfer rate, wherein the storage area of the synchronous storage device is larger than the second storage area of the asynchronous storage device.